

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: DeWitt, Jr. et al.	§
	§ Group Art Unit: 2193
Serial No.: 10/675,776	§
	§ Examiner: Vu, Tuan A.
Filed: September 30, 2003	§
	§ Confirmation No.: 6262
For: Method and Apparatus for	§
Counting Execution of Specific	§
Instructions and Accesses to Specific	
Data Locations	

35525

PATENT TRADEMARK OFFICE
CUSTOMER NUMBER

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

COMMENTS ON STATEMENT OF REASONS FOR ALLOWANCE

Sir:

This "Comments on Statement of Reasons for Allowance" is submitted in reply to the Notice of Allowance mailed on December 29, 2010.

No fees are believed to be required. If, however, any fees are required, I authorize the Commissioner to charge these fees which may be required to IBM Corporation Deposit Account No. 09-0447.

COMMENTS ON REASONS FOR ALLOWANCE

In the Examiner's statement of reasons for allowance on page 4 of the Notice of Allowance mailed December 29, 2010, the Examiner states:

Based on the above, no prior art has been identified to teach or suggest 'instruction cache unit' as illustrated by Figure 31 and expressed by the Applicant's facts based reasoning.

Notice of Allowance, December 29, 2010, p. 4.

Applicants respectfully submit that claim 1 is limited only by the features as set forth explicitly therein. Any features recited in the specification or the Figures should not be imported to the broader independent claim 1.

It is respectfully requested that this "Comments on Statement of Reasons for Allowance" be made of record in the file of the application.

Date: February 23, 2011

Respectfully submitted,

/Brandon G. Williams/

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